

CURRICULUM VITAE

Instructor's Name: FIBY PHILIP

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EDUCATION

MS Physics, University of Texas at Dallas, Richardson, TX (1999)

MSc Physics (Solid State Electronics), University of Gandhiji, Kerala, India (1986)

BSc Physics, Kerala University, India (1984)

PROFESSIONAL EXPERIENCE

Test Engineer – Maxim Integrated Products (2001-2011)

Involved with test development of Microcontrollers with embedded memory circuits such as EPROM, SRAM, ROM and Flash

- Added new tests to the test program upon design or process change and debugged the test program.
- Created and debugged DC and AC characterization program.
- Facilitated the qualification process and released the test program for production environment.
- Revised the test program for yield enhancement and test time reduction.

Product Engineer – Dallas Semiconductor (1999-2001)

Product responsibility of 8051 microcontroller products starting from Fab to Final test to resolve any product issues.

- Facilitated the qualification process of these products upon process/design changes.
- Wrote the program in assembly language, debugged the functional failures using the evaluation board for the yield enhancement.
- Investigated the root cause of failure, identifying the area of problem and implemented corrective action.